

ABSTRACT OF THE DISCLOSURE

A controller 7A for generating a drive signal (PWM signal) PS for controlling a switching power supply, comprises control signal setting means 10, 11 for setting a control signal CS on the basis of an output voltage  $V_o$  and a target voltage  $V_r$ ; current estimating means 12 for estimating an inductor current on the basis of the drive signal PS and for generating an estimated current signal PC; DC component removing means 14, 15 for extracting a DC component DC included in the estimated current signal PC and for removing the DC component DC from the estimated current signal PC; DC component resetting means 13, 14 for resetting the extracted DC component DC every predetermined time; and comparing means 16 for comparing the control signal CS with the estimated current signal PC' after the removal of the DC component DC.